

## DSP Hardware, Software & Algorithms

### Available Now!

Develop digital camera applications rapidly with a Camera Developer's Kit (CDK) from D3 Engineering. The CDKs are designed to plug into Texas Instruments DSK or EVM Kits™ and have you up and running in 15 minutes.

#### Available on 3 Major Development Platforms

- CDK for DSK Boards
- CDK for DM642EVM
- CDK for DM6446EVM (1st Qtr 2007)

#### Everything You Need

The CDKs are complete Imaging Hardware and Software systems available with a variety of imagers and LCDs. They are designed to work with numerous TI DSP's, so the system is reusable should you change applications or platforms.

#### Software

Included software files lets you capture and display images, as well as set up the imager & LCD parameters. The USB 2.0 port allows image data to be rapidly downloaded to an included Windows-based program. A Window's DLL in the software kit gives you the ability to develop your own custom applications.

Software libraries are included for LCD display, LCD GUI routines, image capture and processing. Example programs are furnished that utilize Code Composer Studio to download bitmaps from the imager and upload them to the LCD.

#### Full Reference Designs

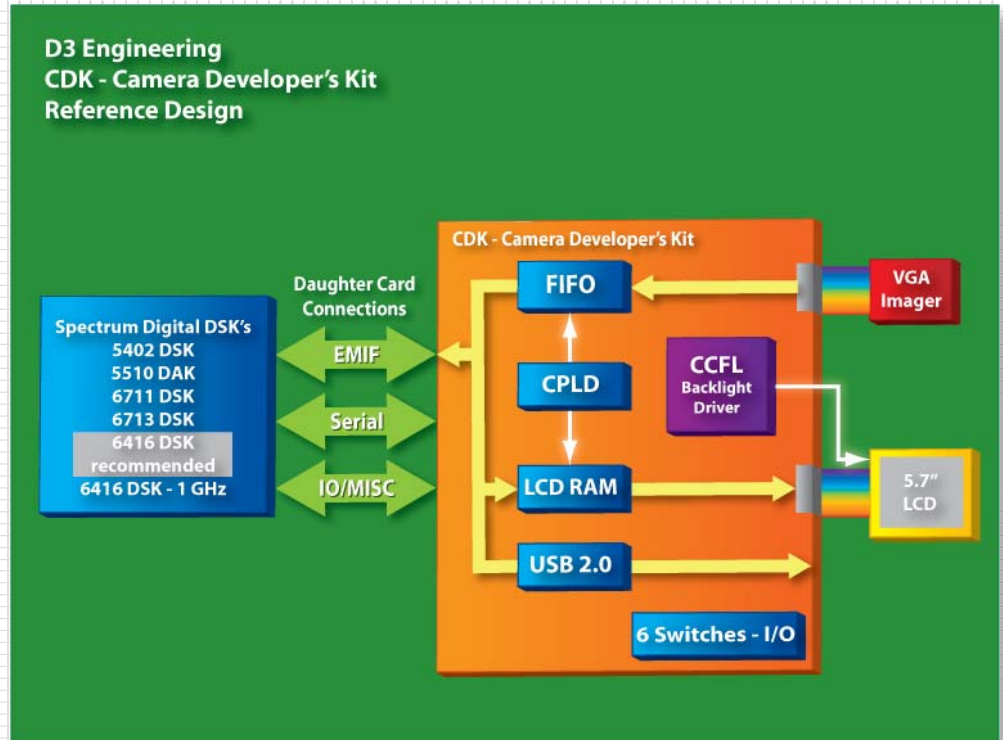
The CDKs are full reference designs and include example code for each DSK or EVM, full schematics, layout and CPLD code. It also incorporates means to enable 6 user input switches.

#### Custom Algorithms Available

The CDK software kit includes examples utilizing the TI Image Processing (IMGLIB) Library. If you need additional assistance, D3's engineers have significant algorithm design capabilities. They can provide custom algorithms for the CDK, such as: JPEG2000, MPEG2, MPEG4, or H.26x. We can also create a customized CDK for your application (Barcode, Machine Vision, OCR, Medical, Surveillance).



### CDK - Camera Developer's Kit

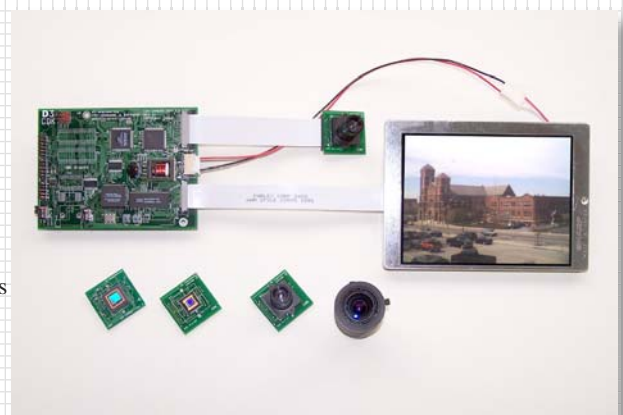


#### Standard Configuration

- 640 x 480 Color CMOS Imager (capable of 30 FPS capture)
- 320 x 240 5.7" TFT-LCD (64k colors, 60Hz refresh)

#### Features

- Complete Schematics, PCB, BOM, and VHDL code for the CPLD.
- Step-by-step examples show how to place your image processing routines into the included code and test your algorithms.
- Included software minimizes image capture processing time and resources.
- Captured pictures or live video out to LCD
- USB 2.0 port for quick downloads
- Royalty-free example software included for:
  - All platforms
  - Code Composer 3.1
  - Windows DLL for USB 2.0 Bulk Mode Transfers



#### Optional Configurations

- 0.3 Mpixel – VGA – 640 x 480 (Color or Mono)
- 1.3 Mpixel – SXGA – 1280 x 1024 (Color or Mono)
- 2.0 Mpixel – UXGA – 1600 x 1200 (Color)
- 3.1 Mpixel – QXGA – 2048 x 1536 (Color)
- 5.1 Mpixel – QSXGA – 2592 x 1944 (Color)
- High Speed – 100 x 120 pixels, 560 FPS
- 3.5" & Other LCD's available